

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1542	345/76.ccls.	US-PGPUB; USPAT	OR	ON	2006/11/14 15:12
L2	280	l1 and sampl\$3	US-PGPUB; USPAT	OR	ON	2006/11/14 15:18
L3	101	l2 and @ad<="20020227"	US-PGPUB; USPAT	OR	ON	2006/11/14 15:18
L4	160	l1 and memory near3 circuit	US-PGPUB; USPAT	OR	ON	2006/11/14 16:25
L5	55	l4 and @ad<="20020227"	US-PGPUB; USPAT	OR	ON	2006/11/14 15:05
L6	37	l5 not l3	US-PGPUB; USPAT	OR	ON	2006/11/14 15:03
L7	2293	315/169.3.ccls.	US-PGPUB; USPAT	OR	ON	2006/11/14 15:08
L8	198	l7 and memory near3 circuit	US-PGPUB; USPAT	OR	ON	2006/11/14 15:18
L9	110	l8 and @ad<="20020227"	US-PGPUB; USPAT	OR	ON	2006/11/14 16:25
L10	81	l9 not l4	US-PGPUB; USPAT	OR	ON	2006/11/14 15:06
L11	1347	315/169.1.ccls.	US-PGPUB; USPAT	OR	ON	2006/11/14 15:08
L12	122	l11 and memory near3 circuit	US-PGPUB; USPAT	OR	ON	2006/11/14 15:08
L13	77	l12 and @ad<="20020227"	US-PGPUB; USPAT	OR	ON	2006/11/14 15:08
L14	65	l13 not l4	US-PGPUB; USPAT	OR	ON	2006/11/14 15:08
L15	773	345/82.ccls.	US-PGPUB; USPAT	OR	ON	2006/11/14 15:12
L16	157	l15 and sampl\$3	US-PGPUB; USPAT	OR	ON	2006/11/14 15:12
L17	71	l16 and @ad<="20020227"	US-PGPUB; USPAT	OR	ON	2006/11/14 15:12
L18	153	345/90.ccls. and sampl\$3	US-PGPUB; USPAT	OR	ON	2006/11/14 16:25
L19	113	l18 and @ad<="20020227"	US-PGPUB; USPAT	OR	ON	2006/11/14 16:18
L20	28	l19 and memory near3 circuit	US-PGPUB; USPAT	OR	ON	2006/11/14 16:18
L21	616	345/98.ccls. and sampl\$3	US-PGPUB; USPAT	OR	ON	2006/11/14 16:18

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L22	148	I21 and memory near3 circuit	US-PGPUB; USPAT	OR	ON	2006/11/14 16:18
L23	100	I22 and @ad<="20020227"	US-PGPUB; USPAT	OR	ON	2006/11/14 16:19
L24	84	I23 not I19	US-PGPUB; USPAT	OR	ON	2006/11/14 16:19
L25	659	345/204.ccls. and sampl\$3	US-PGPUB; USPAT	OR	ON	2006/11/14 16:26
L26	129	I25 and memory near3 circuit	US-PGPUB; USPAT	OR	ON	2006/11/14 16:25
L27	71	I26 and @ad<="20020227"	US-PGPUB; USPAT	OR	ON	2006/11/14 16:26
L28	45	345/204.ccls. and (sampl\$3 same memory near3 circuit)	US-PGPUB; USPAT	OR	ON	2006/11/14 16:26
L29	28	I28 and @ad<="20020227"	US-PGPUB; USPAT	OR	ON	2006/11/14 16:35
L30	1330	mikami-y\$.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/11/14 16:36
L31	28	I30 and memory adj2 circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/11/14 16:36
L32	30	I30 and memory adj2 circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/11/14 16:38
L33	3	(display and memory adj2 circuit and bias adj voltage near3 block\$3).clm.	US-PGPUB	OR	ON	2006/11/14 16:42
L34	61	(display and memory adj2 circuit with pixel).clm.	US-PGPUB	OR	ON	2006/11/14 16:42
L35	15	(display and memory adj2 circuit with pixel and sampl\$3).clm.	US-PGPUB	OR	ON	2006/11/14 16:43